PCF850 - Advanced PC/104+ Bus Analyzer / Exerciser

Analyzer

- Capture Bus Activity
- Event Recognition
- Complex Triggering and Filtering
- Time Stamping and Measurement
- State and Waveform Displays
- Power Zoom (533 Mhz)

Exerciser

- Memory, I/O, Config Transfers
- Generate Test Patterns
- Configuration Scanning
- Control Address / Data Width
- Read / Write to a File

Stimulus

- Fault Injection
- Control Bus Timing
- Hardware Simulation
- Pattern Generation
- Drive any Signal

Target Memory

- Windowed Bus Memory
- Split, Retry, Disconnect Response

Protocol Violation Checker

- Detects >50 Protocol Violations
- Listed in State/Waveform Display
- Used as Trigger / Filter

Timing Violation Checker

- Checks Unstable Signals
- Setup and Hold Verification
- Glitch Detection

Performance Analysis

- Bus Utilization
- Transfer Rate
- Latency
- Burst Distribution
- Statistics

Compliance Testing

PCISIG Checklist

Windows and API Interface

- Analyzelt Windows Software
- User programmable API

Expansion Connector

AD 227ns 10ns 170 Wi 00000061 20 I/O Wr 00000064 120ns TI AB 94ns 08 11ns

PCF850

Analyze It!

Anomaly Detection

🗸 Compliance Tests

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PC/104+ **Bus Analyzer**

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The PCF850 analyzes and exercises the 32 bit PCI bus in PC/104+ systems running at 0 to 133 Mhz. Over 100 protocol and timing violations are automatically checked and correlated with captured bus activity. System performance measurements include Bus Utilization, Transfer Rates, Latency, and Statistics.

Analyze It! Windows Software

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Target - 🗆 × Stimulu: - 🗆 × 🕇 Mastr - 🗆 🗵 Trace - Trigger1 🔳 🧮 Trace - Trigger2 <u>- 🗆 ×</u> ■→▲· 60.25u L1 <u>S</u>etup IDLE <New: TIME[17:0] DELTA BUS DOCTOR SAMPLI COMMAND ADDRESS32 DATA32 000(• 1 0000 0000 0000 0000 0000 0000 I/O Rd 00000078 643ns 80 60.25us 53ns 351ns I/O Wr 00000020 0000 20 10ns 59.17u 0000 0000 00000000 0000 12 I/O Rd 00000064 981ns 13 1D 53ns 0000

Silicon Control introduces the ultimate analyzer and exerciser for PC/104+ systems. This 3rd generation PC/104+ analyzer combines high performance hardware with a sophisticated and intuitive software interface. The result is a powerful diagnostic tool for bus analysis all on a single plug-in card.

SILICON CONTROL INC.

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THE LEADERS IN BUS ANALYSIS

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PCF 850 SPECIFICATIONS

General Specifications

PCI Compliance: Bus Size: Bus Signal Levels:

Sampled Signals:

Trace Specifications

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Trace Memory:	
PCF850-1	128K by 144 bits
PCF850-2	256K by 144 bits
PCF850-3	512K by 144 bits
PCF850-4	1M by 144 bits
PCF850-5	2M by 144 bits
Sampling Rate:	0 to 133 Mhz
High speed power zoom	533 Mhz

Sampling Modes: System Clock System Clock w/ Address/Data System Clock w/ Transfers On board precision Oscillator (7.5ns to 15us)

32 bit 5V or 3.3V

PCI 2.2, PCI-X 1.0 Compliant

AD[31:0], C/BE[7:0], FRAME,

DEVSEL, TRDY, IRDY, PAR, REQ, GNT, RST, LOCK, CLK, INTA, INTB, INTC, INTD, PAR64, PERR, SERR, REQ64, ACK64, TDO, TDI, TCK, TMS, TRST, SDONE, SBO, EXT[7:0] **External Inputs:** 8 Front Panel Trace/Trigger **External Outputs:** 1 Programmable Trigger Output Triggers: 8 Trigger Conditions each Specifying 100 PCI Signals, 8 External Triggers and Anomaly Trigger Types: Single Condition

Logical Combination
16 Level SequencerTrigger Positions:0%, 25%, 50%, 75%, 100%Occurrence Counters:16 hardware counters 20 bits

Event Counters:16 hardware counters 20 bitsTime Tag:7.5 ns to 60 sec.

Exerciser Specifications

Initiator Bandwidth:	1056 MB/s rate
Initiator Bus Width:	64 or 32 bit
Initiator Transfers:	Memory, I/O, Configuration

Target Specifications

Target Specifications	
Target Memory:	1.10
PCF850-1	1 MB
PCF850-2	2 MB
PCF850-3	4 MB
PCF850-4	8 MB
PCF850-5	16 MB
Target Bandwidth:	1056 MB/s burst rate
Target Bus Width:	64 or 32 bit
Front Panel Interfaces	
RS232 Port:	DB9 connector,
R5252 1 01t.	110 to 115K Baud
	(cable included)
USB Port:	Series B connector, 12 MB/s
CDD Fort.	(cable included)
Indicators:	GO LED, User LED
indicatoro.	
Pushbutton:	Reset Analyzer or System
External Power:	2 Conductor front panel
	(cable included)
Trigger:	10 pin socket
	(8 in, 1 out, 1 ground)
_	(cable included)
Fuses:	Main power and External power
Power Requirements	
-	Operating—5V at 3 Amps max
	Standby—5V at 1 Amp max
Dimonstone	
Dimensions	by 2.22 in to allow a DMC 250 prod
uct to be adapted)	by 2.32 in, to allow a PMC850 prod- Single PC/104+ Card
uet to be adapted)	Height: 23.3 mm (0.92 in)
	Width: 96.0 mm (3.78 in)
	Depth: 149.0 mm (5.87 in)
Ordering Information*	
PC/104+ Bus Analyzer/E	xerciser
PCF850-1	128K Trace Buffer
	1 MB Target Memory
PCF850-2	256K Trace Buffer
	2 MB Target Memory
PCF850-3	512K Trace Buffer
	4 MB Target Memory
PCF850-4	1M Trace Buffer
	8 MB Target Memory
PCF850-5	2M Trace Buffer
	16 MB Target Memory

* Includes AnalyzeIt and API software, USB, RS232, trigger and external power cables, documentation, carrying case and 2 year warranty